(19) JAPANESE PATENT OFFICE

## PATENT ABSTRACTS OF JAPAN

(11) Publication number: **04167820 A**

(43) Date of publication of application: 15 . 06 . 92

| (51) Int. Cl | H03M 7/14 |
| | G11B 20/18 |

(21) Application number: 02294099

(22) Date of filing: 31 . 10 . 90

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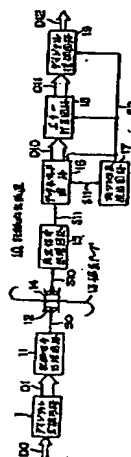(72) Inventor: ISOZAKI MASAAKI

(54) DIGITAL DATA TRANSMISSION METHOD

(57) Abstract:

PURPOSE: To improve an error rate at demodulation by replacing a violation data string with a closest regular data and demodulating the result when the data string in violation with the modulation rule exists in a sent modulation data.

CONSTITUTION: An input data D0 is modulated at a modulation circuit 1 based on a prescribed modulation rule and a modulation data D1 is inputted to a signal processing circuit 11. The circuit 11 adds a preamble, a post-amble and a synchronous signal to the data D1 and applies parallel/serial conversion to the result, a resulting recording signal S0 is fed to a recording head 12, by which a recording pattern is formed. A regenerative signal S10 from a reproducing head 14 is fed to a reproduction processing circuit 15, from which a digital signal S11 is outputted to a detection circuit 17 via a holding circuit 16 and a reproduction data D10 is given to an error correction circuit 18. The circuit 17 detects a block location in the signal S11 and outputs the result to the circuits 16, 18 and a demodulation circuit 19, and when there is a data string in violation with the modulation rule in a data D10, the circuit 18 replaces the data string with a closest data string and outputs the result.